

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

(1) Cancel claim 1 has been cancelled.

(2) Claim 2 has been rewritten as follows:

1 2. (Amended) [The circuit of claim 1 further comprising an analog to digital converter
2 connected between said amplifier and said output] A circuit comprising:

3 a. an input,

4 b. an output,

5 c. a chopper stabilized, multistage feedforward amplifier connected between said input
6 and said output, and

7 d. an analog to digital converter connected between said amplifier and said output.

1 (3) Claim 11 has been rewritten as follows:

2 11. (Amended) The circuit of claim [1] 2, further comprising a plurality of integrators connected
3 between said amplifier and said output.

(4) Claim 12 has been rewritten as follows:

1 12. (Amended) The circuit of claim [1] 2 fabricated on an integrated circuit.

(5) Claim 28 has been cancelled.

(6) Claim 29 has been rewritten as follows:

1 29. (Amended) [The] A method of [claim 28 further] designing an integrated circuit comprising
2 the [step] steps of:
3 specifying an input, an output and a chopper stabilized, multistage, feedforward amplifier
4 connected between said input and said output; and
5 specifying a delta sigma modulator to be connected between said amplifier and said output.

(7) Claim 30 has been cancelled.

(8) Claim 31 has been rewritten as follows:

1 31. (Amended) [The] A method of [claim 30 further] fabricating an integrated circuit comprising
2 the [step] steps of:
3 providing an input, an output and a chopper stabilized, multistage, feedforward amplifier
4 connected between said input and said output; and
5 providing a delta sigma modulator to be connected between said amplifier and said output.